



College of Engineering

DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING

Title of the Project goes here!

Final Capstone Design Report

Capstone Design Group 04

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Date of Submission: October 11, 2026

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Certificate of Approval

This report entitled, prepared and submitted by the following team members in partial fulfillment of the requirements of the degree program, has been approved and accepted by the examining committee.

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Date: October 11, 2026

Abstract

This capstone project presents the design, implementation, and validation of ...

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Introduction

1.1 Project Overview

Your main report text begins here...

Furthermore, rapid transient simulation and macromodeling algorithms [1] enable faster fault isolation. All third-party documentation and references utilized in this document comply with institutional fair dealing criteria [2].

A sample formula (1.1) is provided below for demonstration purposes:

$$E = mc^2 \tag{1.1}$$

As a demonstration of a figure, Figure 1.1 is included below:



Figure 1.1: Example Figure Caption.

As a demonstration of a table, Table 1.1 is included below:

Table 1.1: Example Table Caption.

Column 1	Column 2	Column 3
Data 1	Data 2	Data 3
Data 4	Data 5	Data 6

Bibliography

- [1] F. Lastname1 and F. Lastname2, “Passivity-preserving macromodeling for high-speed interconnects,” *IEEE Transactions on Components, Packaging and Manufacturing Technology*, vol. 3, no. 8, pp. 1385–1395, 2013.
- [2] University of Saskatchewan, “Fair dealing guidelines.” <https://library.usask.ca/copyright/guidelines/fair-dealing.php>, 2024.